

PRESS RELEASE

Pintail Technologies and optimiSE Announce Global Marketing and Technology Alliance

Pintail to Market Test Improvement Software Solutions from optimiSE

DALLAS (April 10, 2006) - Pintail Technologies of Dallas, Texas and optimiSE GmbH of Karlsruhe, Germany announced today that they have entered a world-wide marketing and technology agreement. Pintail and optimiSE are both leaders in semiconductor test improvement and yield learning software solutions. The agreement calls for Pintail to market all of the optimiSE test improvement solutions to the semiconductor industry on an exclusive, worldwide basis. The two companies will collaborate on advanced development in the field of adaptive test and statistical process control techniques that optimize semiconductor testing.

"We are very pleased to be aligning ourselves with the Pintail team," stated Dr. Gunther Karner, president of optimiSE. "Pintail's unique method of capturing and analyzing data in real-time is leading the market in innovation and value. We are excited to be able to join forces and extend the collective test improvement capabilities with our comprehensive redundancy and correlation analysis. The increased value proposition provides a compelling solution that will be very effective at reducing the cost of test."

"Leading semiconductor manufacturers throughout Europe recommended optimiSE as a "must have" tool for semiconductor test," stated Taylor Scanlon, president and CEO of Pintail. "The optimiSE team brings a new dimension of mathematical methods in nonparametric analysis and statistical process control to the mission of modern semiconductor testing. Combining optimiSE's advanced analysis capabilities with Pintail's production proven real-time test, customers will realize unmatched ability reduce the cost of test, improve quality levels and improve productivity."

"Having reviewed the technical roadmaps of both Pintail and optimiSE, I believe the collaboration between these two innovators will propel the acceptance of real-time SPC methods to some of the most interesting challenges facing the semiconductor industry," stated Chris Portelli, Worldwide Final Manufacturing Test Strategy Manager, for STMicroelectronics, Rousset, France. "Pintail's statistical sampling is just part of a whole range of adaptive and feed-forward techniques contributing to yield learning and test optimization."

"We have successfully used the technology and achieved noteworthy savings," stated Hans Martin von Staudt, Manager Test and Product Engineering of Dialog Semiconductor in Germany. "We expect a lot from combining Pintail's real-time software and optimiSE's analytical capabilities to further reduce our cost of testing."

SwifTest™ is the real-time monitoring and statistical engine at the heart of Pintail's broad product offering that addresses test time reduction, reliability improvement and OEE improvement for operations. When combined with TestScope™, the Pintail suite also gives ultra-fast visibility and control to test engineers

managing global outsourcing of their test requirements. Pintail's solutions are in production today at leading fabless and integrated device manufacturers (IDMs). SwifTest™ is installed at dozens of test subcontractors throughout Asia supporting the shipment of tens of millions of devices each month. SwifTest is currently available on Teradyne Catalyst, FLEX and J750 testers, as well as LTX Fusion, Agilent 93K, and Credence Duo & Quartet

reduTec™ is the premier analysis tool developed by optimiSE over the past several years. reduTec combines a number of sophisticated mathematical models to determine the best test plan to be used with Pintail's real-time sampling engine. reduTec can take test data and determine correlations between individual tests and between a test and a group of tests as well as detecting complex levels of redundancy within the test program. optimiSE's proprietary "knowledge gain" analysis functionality assures maximum reliability for highly optimized test programs.

In addition to reduTec, optimiSE also has a practical utility tool called STDF Browser. STDF Browser allows users to very efficiently examine and analyze STDF files in order to see exactly what data is in their files without making restrictive assumptions about the compliance with STDF guidelines. Since there is significant variability between STDF outputs, users report that this tool is a critical utility for anyone dealing with modern semiconductor test data. A free 90-day trial offer of this tool is available on Pintail's website (see below for URL).

optimiSE also fields a suite of tools that optimize the testing of board-level and system-level products. optimiSE will continue to market these solutions directly to the electronic manufacturing services sector.

About Pintail Technologies

Pintail Technologies is a semiconductor test improvement and yield learning company that provides breakthrough software to reduce the cost of test and increase production output and quality through existing test equipment and programs. Pintail has created the first test improvement solutions to combine real-time test process control capabilities with innovative analytical programs in an integrated software suite designed for speed and accuracy. Fabless and integrated device manufacturers worldwide, in collaboration with leading assembly and test suppliers use Pintail tools to increase competitive advantage and improve responsiveness to test issues. Pintail has sales offices in Dallas, Austin, San Jose, Seoul, Singapore, Taiwan, Munich and Switzerland. See more information at www.pintail.com.

About optimiSE

Founded in 2001, optimiSE is dedicated to the development of sophisticated software applications that reduce the ever increasing cost of test in the semiconductor industry and in the electronic manufacturing service industry. optimiSE's mission is the "Closed Loop Test Enhancement" from the unprocessed wafer to the assembled electronic device. To this end, feed forward solutions such as PCM -> Wafer Probe or ICT Board Test -> Functional Board Test have been developed and applied by leading manufacturers in the electronics industry. See more information at www.optimise.de

